

Docket No.: 49657-911



2D31

PATENT

#2
6-15-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yoshihito OCHI, et al.

Serial No.: 09/764,137

Group Art Unit: 2123

Filed: January 19, 2001

Examiner:

For: CIRCUIT SIMULATING APPARATUS PERFORMING SIMULATION WITH
UNNECESSARY CIRCUIT DISCONNECTED

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents
Washington, DC 20231

RECEIVED

JUN 6 2001

Technology Center 2100

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the Title is incorrect on the Filing Receipt. Attached is a copy of the Declaration and Power of Attorney, which evidences that the correct title of the application should read: **Circuit simulating apparatus performing simulation with unnecessary circuit disconnected.**

It is requested that a corrected Filing Receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/764,137	01/19/2001	2123	710	49657-911	10	20	3

CONFIRMATION NO. 8210

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MAR 12 2001

FILING RECEIPT



OC000000005827304

McDermott, Will & Emery

Date Mailed: 03/05/2001

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Applicant(s)

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Yoshiki Nakamura, Hyogo, JAPAN;

Assignment For Published Patent Application

Mitsubishi Denki Kabushiki Kaisha and Mitsubishi Electric System LSI Design Corporation,;

Continuing Data as Claimed by Applicant

Foreign Applications

JAPAN 2000-215558 07/17/2000

If Required, Foreign Filing License Granted 03/05/2001

Projected Publication Date: 01/17/2002

Non-Publication Request: No

Early Publication Request: No

Title

Circuit
Circuit simulating apparatus performing simulation with unnecessary circuit disconnected

Preliminary Class

703

Data entry by : PETROS, TADELECH

Team : OIPE

Date: 03/05/2001





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CONFIRMATION NO. 8210



Bib Data Sheet

SERIAL NUMBER 09/764,137	FILING DATE 01/19/2001 RULE	CLASS 703	GROUP ART UNIT 2123	ATTORNEY DOCKET NO. 49657-911	
APPLICANTS Yoshihito Ochi, Hyogo, JAPAN; Tetsuya Muta, Hyogo, JAPAN; Yoshiaki Nakamura, Hyogo, JAPAN;					
** CONTINUING DATA ***** ** FOREIGN APPLICATIONS ***** JAPAN 2000-215558 07/17/2000					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 03/05/2001					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance met		STATE OR COUNTRY JAPAN	SHEETS DRAWING 10	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
Verified and Acknowledged Examiner's Signature _____ Initials _____					
ADDRESS McDERMOTT, WILL & EMERY 600 13th Street, N.W. Washington, DC 20005-3096					
TITLE Circuit simulating apparatus performing simulation with unnecessary circuit disconnected					
FILING FEE RECEIVED 710	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		

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